

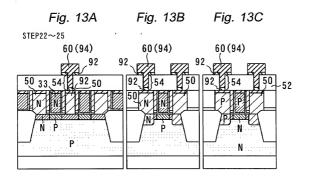
Fig. 12A Fig. 12B Fig. 12C

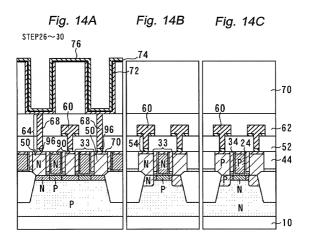
STEP19~21

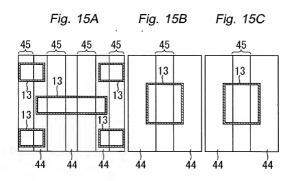
90 (88) 90 (88) 90 (88) 90 (88) 90 (88) 90 (88)

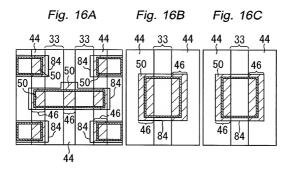
NN P P P N N

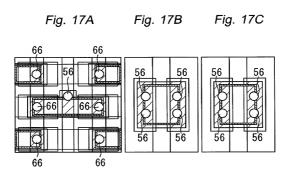
(a) (b) (c)

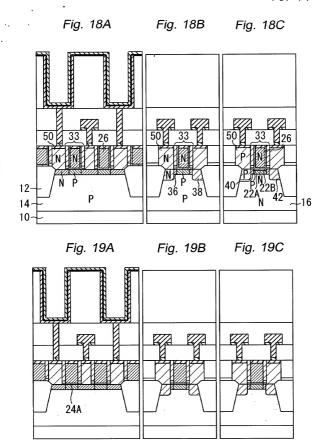


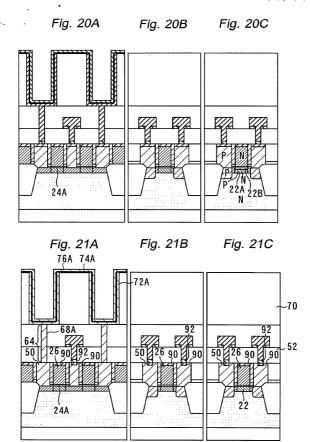


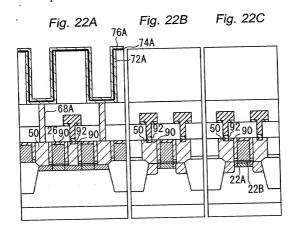


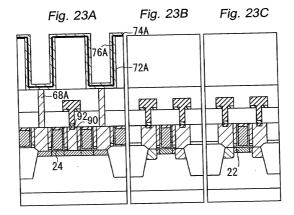


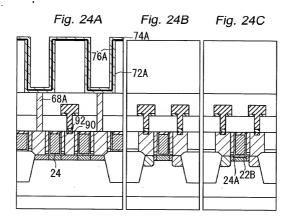


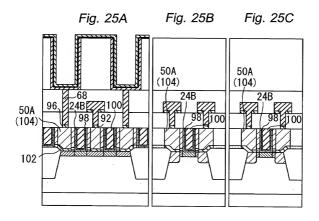












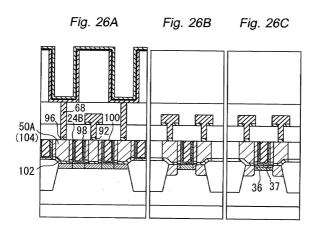


Fig. 27A Fig. 27B Fig. 27C PRIOR ART PRIOR ART PRIOR ART 76 74 72 -70 60 60 32 (34) 58 -62 46 -52 30 -44 32 (34)12-P 28 26 , р 38 14 -16 10-14 MEMORY CELL NMOS TRANSISTOR PMOS TRANSISTOR SECTION SECTION SECTION

